CLAIMS

1. An array substrate for a flat display device comprising:

a display unit in which a pixel is placed at each of intersections between a plurality of signal lines and a plurality of scan lines, the signal and scan lines being routed in the form of a matrix;

a plurality of output lines configured to output data signals to the signal lines, respectively;

a plurality of switches placed between the output lines and the signal lines to connect each output line to n signal lines (n is an integer equal to or greater than two) one after another within one horizontal period;

n switch control signal lines configured to supply control electrodes of the switches with control signals for controlling on and off states thereof; and

a plurality of electrode patterns configured to connect the control electrode of each switch to any one of the n switch control signal lines,

wherein the electrode patterns each two-dimensionally overlap all of the switch control signal lines and have substantially identical shapes.

2. The array substrate according to claim 1, wherein the electrode patterns and the switch control signal lines are stacked with an insulating layer interposed therebetween, and are electrically connected to each other by contact holes formed in the insulating layer.